ABSTRACT

A network state machine which implements the three network states of HPNA 2.0 in hardware has been disclosed. The network state machine implements the three network states using two network states. When a station is in the V1M2 mode, instead of transmitting this frame in the 10M8 format frame with the gap frame, the frame is transmitted in the 1M8 format frame without any gaps in the frame. By implementing this in hardware, the network state machine has a faster response time.

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